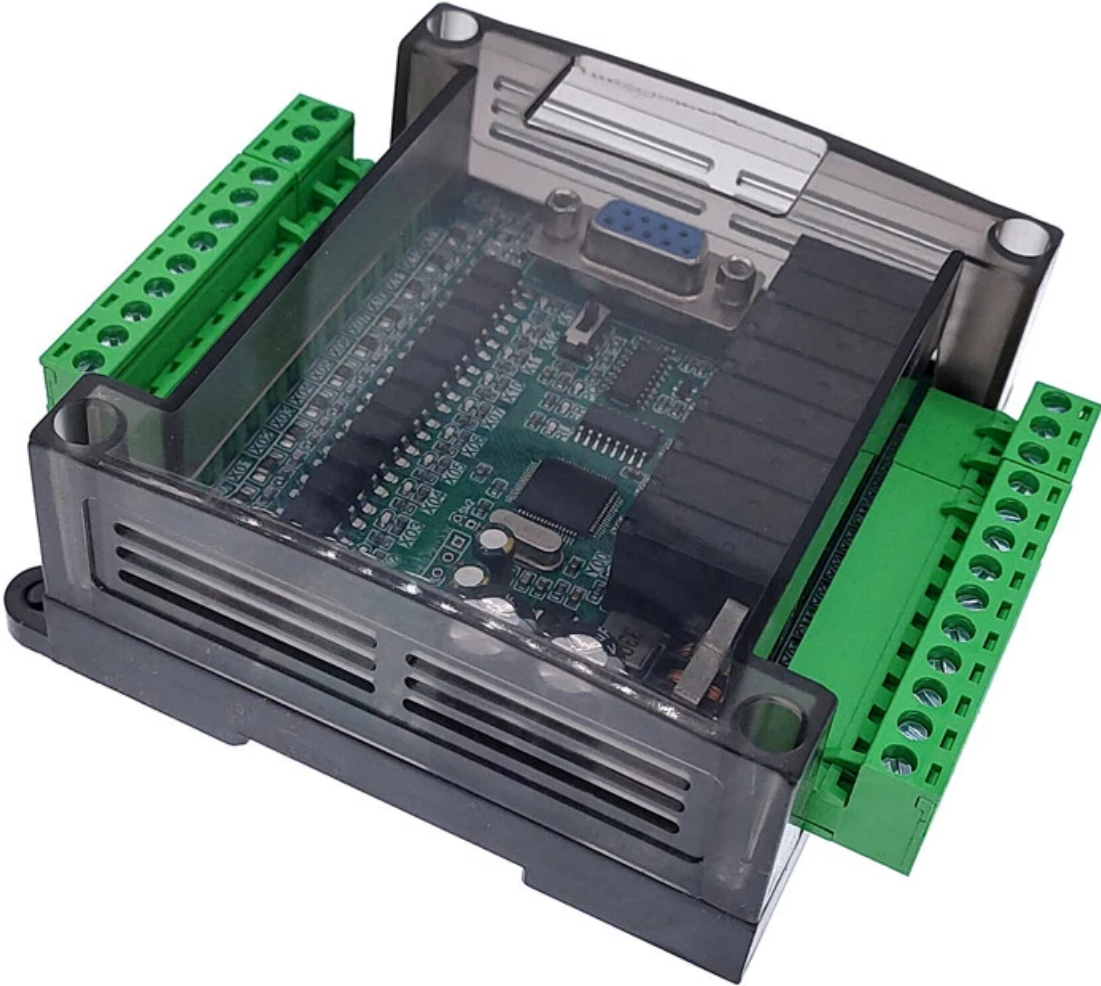
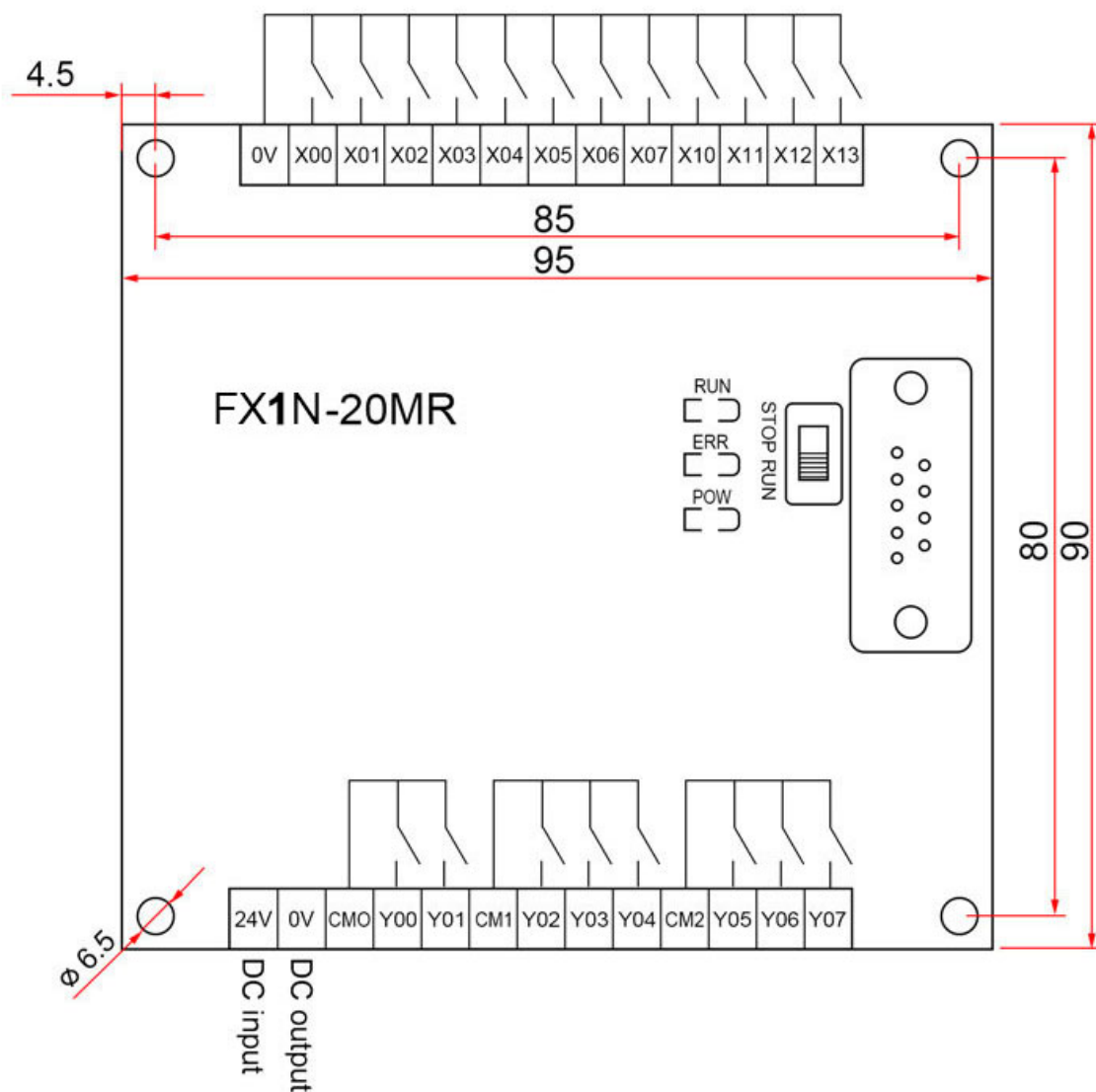


PLC programmable controller 1N-20MR DC Relay module with Base Industrial Control Board Programmable Logic Controller

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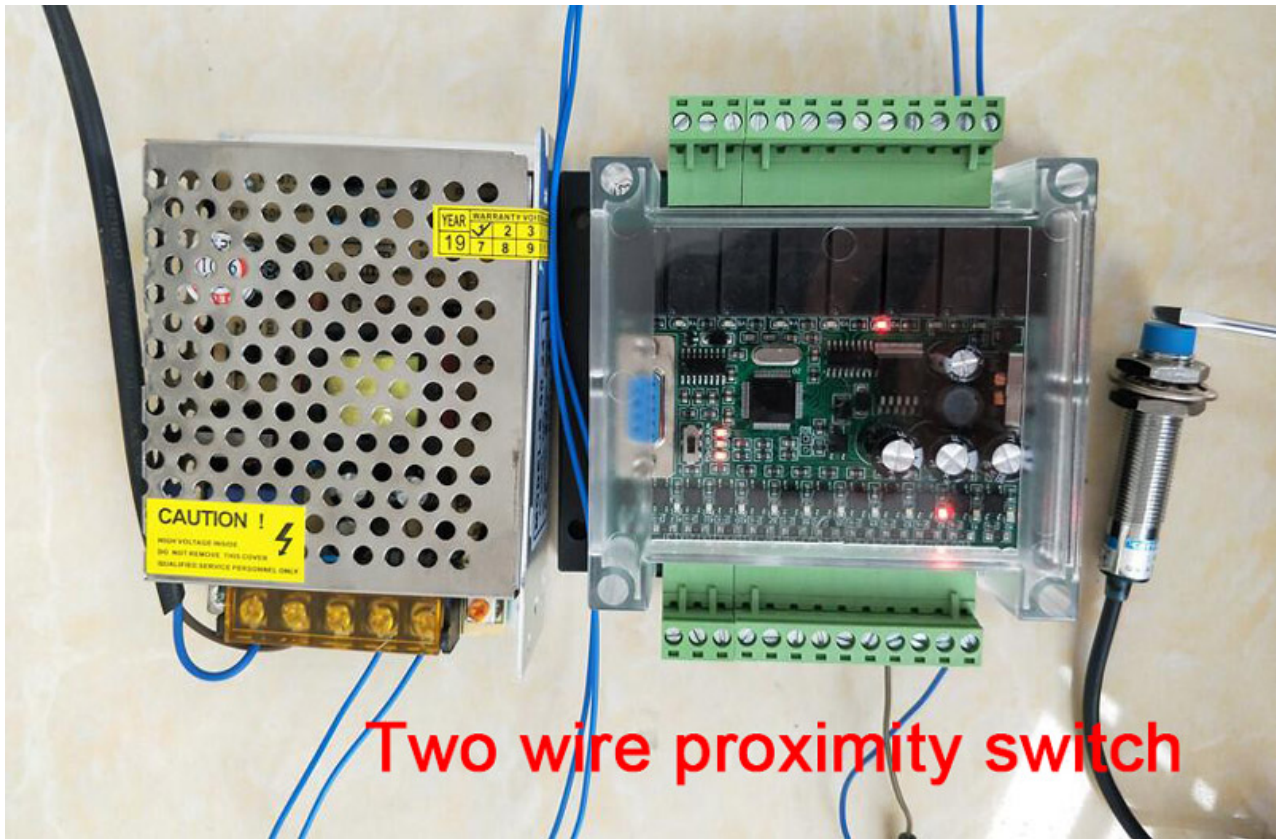


## PLC wiring diagram (XC is 0V):



## The parameters

параметр



PLC wiring diagram as above (XC is 0V)

As long as the touch screen supports fx1n and has 232 interface, you can communicate with this PLC

### **Product function description:**

Write applications in ladder diagram language with support for Mitsubishi GX developer8.xx, GX-Work2.

Working voltage: DC 22V- 28V.

Relay load capacity: AC no more than 220V 5A, DC no more than 30V 5A

Support human-machine interface connection, text display (as long as the touch screen support fx1n can, we have done experiments: excellent control, display control, step, Vilun Tong, Kunlun Tong state, etc.).

Support ladder diagram programming, download, monitoring, batch soft original monitoring, memory clearance.

The programming port is the port to download the program and communicate with the man-machine interface.

This PLC adopts the industrial grade 32 - bit MCU with strong anti - interference.

Password protection, as long as the password is 12345678 internal procedures are protected, to prevent your program from being illegally stolen, protect the fruits of your labor. If the password is set to 12345678, there will be no program reading function on the board. You will only be able to download the program and the "Parameters" option will not be checked. Unable to unlock password lock!!

Download baud rate support: 9600 (default)

Support two wire proximity switch, when the proximity switch is near the screwdriver Y2 input voltage, X2 output voltage

Instruction list						
Classification	Instruction mnemonic	Function description	Classification	Instruction mnemonic	Function description	
Basic sequence control command	LD	take	Circulation and displacement	SFTR	Who moves to the right	
	LDI	Take reverse		SFTL	A shift to the left	
	AND	And		SFWR	FIFO (first in, first out) writes	
	ANI	And reversal		SFRD	FIFO (first in first out) readout	
	OR	or	High speed processing	REF	I/o refresh	
	ORI	Or reverse		REFF	Enter filter time adjustment	
	OUT	output		MTR	Matrix input	
	SET	Set		HSCS	Comparative setting (for high speed counting)	
	RST	reset		HSCR	Comparative reset (for high-speed counting)	
	ANB	Circuit block and		SPD	Pulse density	
	ORB	Loop block or		PLSY	Specify the frequency pulse output	
	MPS	Enter the stack		PWM	Pulse width modulation output	
	MRD	Reading stack	PLSR	With acceleration and deceleration pulse output		
	MPP	Out of the stack	Convenience instruction	IST	State initialization	
	INV	reversal		ABSD	Cam control (absolute)	
	LDP	Take the rising edge of pulse		INCD	Cam control (incremental)	
	LDF	Take pulse falling edge		ALT	Alternate output	
	ANDP	And the rising edge of the pulse	RAMP	Oblique wave signal		
	ANDF	And pulse falling edge	DSW	BCD digital switch input		
	ORP	Ascending edge of vein	SEGL	Seven segment code time - sharing display		
	ORF	Or descending edge of pulse	FROM	BFM read		
	RET	return	TO	BFM write		
	PLS	Rising edge pulse	Peripherals	RS	Serial data transmission	
	PLF	Falling edge pulse		PRUN	Octal bit transfer (#)	
	MC	master control		ASCI	Convert hexadecimal numbers to ASCII codes	
	MCR	Master reset		HEX	ASCII codes are converted to hexadecimal numbers	
	END	end		CCD	check	
	Step instruction	STL		Step ladder diagram	VRRD	Potentiometer variable input
	Procedure flow	CJ		conditional Jump	VRSC	Potentiometer variable interval
		CALL		Subroutine call	PID	PID arithmetic
		SRET	Subroutine return	location	ABS	The current value of ABS reads
		IRET	Interrupt return		ZRN	The origin of regression
EI		On / off	PLSV		Variable pulse output	
DI		Off interrupt	DRVI		Relative position control	
FEND		End of main program	DRVA	Absolute position control		
WDT		Monitor timer refresh	Clock operation	TCMP	Clock data comparison	
FOR		The starting point and number of cycles		TZCP	Clock data interval comparison	
NEXT		The end of the cycle		TADD	Clock data addition	
MOV	delivery	TSUB		Clock data subtraction		
CML	Reverse transmission	TRD		Clock data readout		
XCH	exchange	TWR		Clock data write		
BCD	Binary to BCD	hour	timer			
Transmission and comparison	BIN	Conversion of BCD code to binary	Electric shock comparison	LD=	When (S1) = (S2), the initial contact is switched on	
	CMP	compare		LD>	When (S1) > (S2), the initial contact is connected	
	ZCP	Interval comparison		LD<	When (S1) < (S2), the initial contact is switched on	
	FMOV	Multicast		LD<>	When (S1) <> (S2), the initial contact is switched on	
	SMOV	Bit transfer		LD≤	When (S1) ≤ (S2), the initial contact is switched on	
	BMOV	bulk transfer		LD≥	When (S1) ≥ (S2), the initial contact is switched on	
	ADD	Binary addition		AND=	When (S1) = (S2), series contacts are connected	
	SUB	Binary subtraction		AND>	When (S1) > (S2), the series contacts are connected	
MUL	Binary multiplication	AND<		When (S1) < (S2), the series contacts are connected		
DIV	Binary division	AND<>		When (S1) <> (S2), the series contacts are connected		
INC	Binary plus one operation	AND≤		When (S1) ≤ (S2), the series contacts are connected		
DEC	Binary minus one operation	AND≥		When (S1) ≥ (S2), the series contact is connected		
WAND	The logic of Chinese characters and Chinese	OR=		When (S1) = (S2), parallel contacts are connected		
WOR	Word logic or	OR>		When (S1) > (S2), parallel contacts are connected		
WXOR	Self logical XOR	OR<		When (S1) < (S2), parallel contacts are connected		
NEG	Finding binary complement	OR<>		When (S1) <> (S2), parallel contacts are connected		
Bit data processing	ZRST	Word left	OR≤	When (S1) ≤ (S2), parallel contacts are connected		
	DECO	decode	OR≥	When (S1) ≥ (S2), parallel contacts are connected		
	ENCO	code				



ОПИСАНИЕ

