

25Gbps 850nm SFP28 Optical Transceiver

i28-850-100m

Features

- Hot-pluggable SFP28 form factor
- Supports 25Gbps data rate
- Maximum link length of 70m on OM3 MMF and 100m on OM4 MMF
- 850nm VCSEL laser and PIN photo-detector
- Internal CDR on both Transmitter and Receiver channel
- Duplex LC receptacle
- Single 3.3V power supply
- Power dissipation < 1W
- Digital diagnostics functions are available via the I2C interface
- RoHS-6 compliant
- Commercial case temperature range: 0°C to 70°C



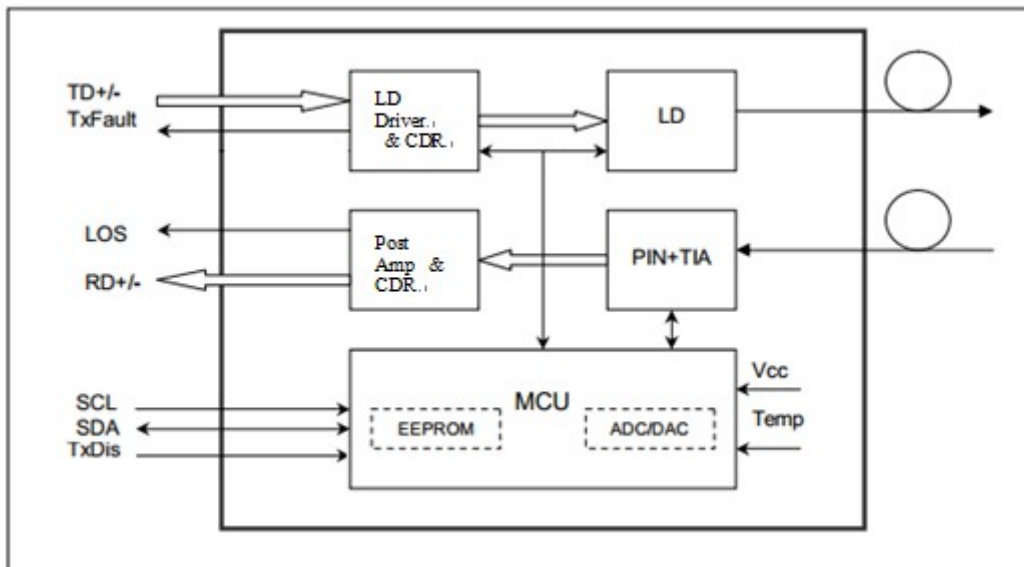
Applications

- 25GBASE-SR Ethernet

Description

The Xmethod Network i28-850-100m is a single-Channel, Pluggable, Fiber-Optic SFP28 for 25 Gigabit Ethernet and Infiniband EDR Applications. It is a high performance module for short-range data communication and interconnect applications which operate at 25.78125 Gbps up to 70 m using OM3 fiber or 100 m using OM4 fiber. This module is designed to operate over multimode fiber systems using a nominal wavelength of 850nm. The electrical interface uses a 20 contact edge type connector. The optical interface uses duplex LC receptacle. This module incorporates Xmethod Network proven circuit and VCSEL technology to provide reliable long life, high performance, and consistent service.

Block Diagram



Absolute Maximum Ratings

Table 1 - Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	0	3.6	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

Recommended Operating Conditions

Table 2 - Recommended Operating Conditions

Parameter		Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Commercial	Tc	0		+70	°C
Power Supply Voltage		Vcc	3.13	3.3	3.47	V
Power Supply Current		Icc			300	mA
Fiber Length on 50/125µm high-bandwidth (OM3) MMF					70	m
Fiber Length on 50/125µm high-bandwidth (OM4) MMF					100	m

Optical and Electrical Characteristics

Table 3 - Optical and Electrical Characteristics

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Transmitter						
Data rate	BR		25.78		Gbps	
Centre Wavelength	λ_c	840	850	860	nm	
Spectral Width (-20dB)	σ			0.6	nm	
Average Output Power	Pavg	-8.4		2.4	dBm	
Optical Power OMA	P _{OMA}	-6.4		3	dBm	
Extinction Ratio	ER	2			dB	
Differential data input swing	V _{IN,PP}	40		1000	mV	
Input Differential Impedance	Z _{IN}	90	100	110	Ω	
TX Disable	Disable		2.0		Vcc	V
	Enable		0		0.8	V
TX Fault	Fault		2.0		Vcc	V
	Normal		0		0.8	V
Receiver						
Data rate	BR		25.78		Gbps	
Centre Wavelength	λ_c	840	850	860	nm	
Receiver Sensitivity (OMA)	P _{sens}	-	-	-10	dBm	
Stressed Sensitivity (OMA)		-	-	-5.2	dBm	
Receiver Power (OMA)				3	dBm	
LOS De-Assert	LOS _D			-13	dBm	
LOS Assert	LOS _A	-30			dBm	
LOS Hysteresis		0.5			dB	
Differential data output swing	V _{out,PP}	300		850	mV	
LOS	High	2.0		Vcc	V	
	Low			0.8	V	

Notes:

Receive Sensitivity measured with a prbs31 pattern @25.78125Gb/s, BER 1E-5; ;

Diagnostics

Table 4 – Diagnostics Specification

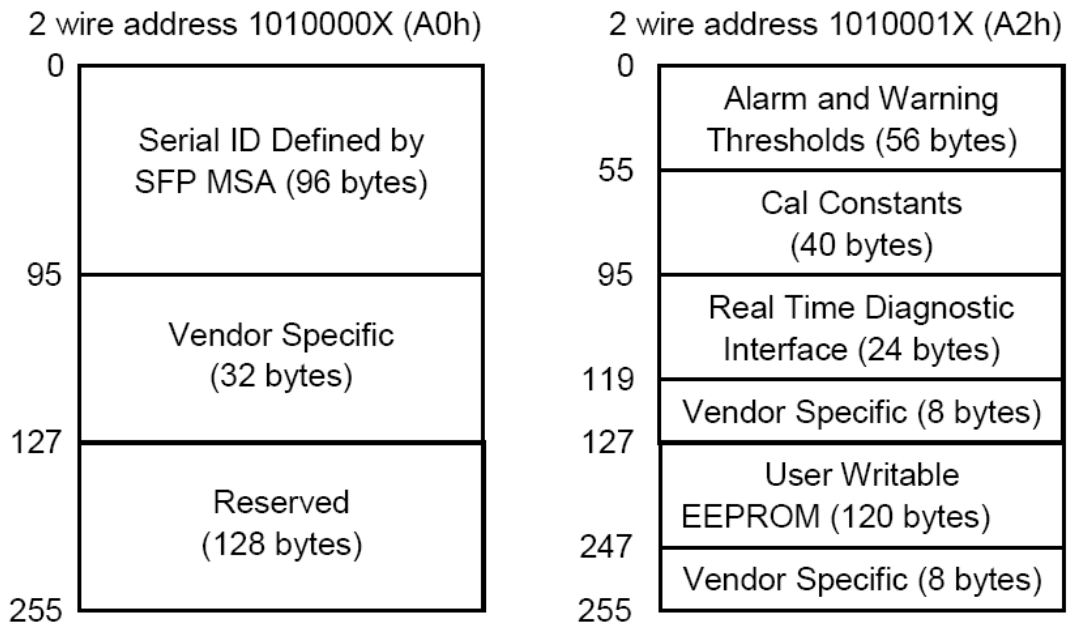
Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal / External
Voltage	3.0 to 3.6	V	±3%	Internal / External
Bias Current	0 to 20	mA	±10%	Internal / External
TX Power	-8 to 3	dBm	±3dB	Internal / External
RX Power	-14 to 0	dBm	±3dB	Internal / External

Digital Diagnostic Memory Map

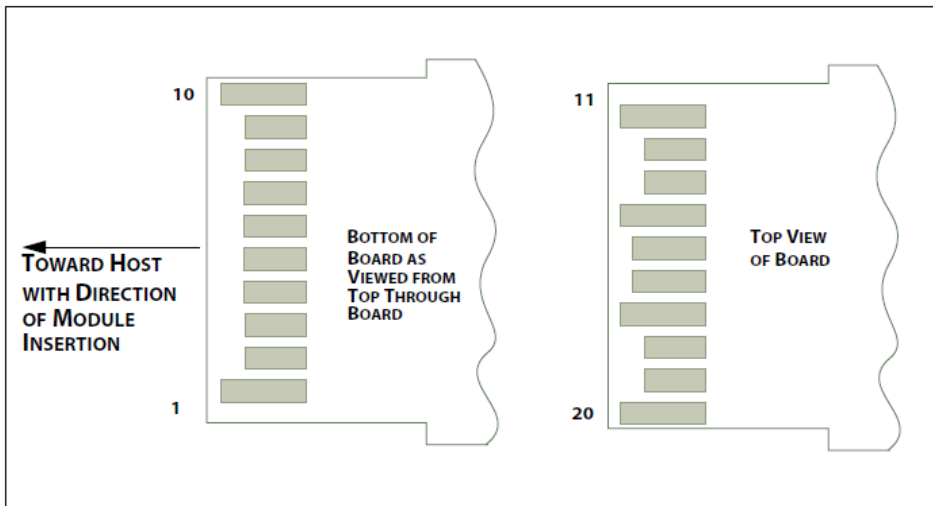
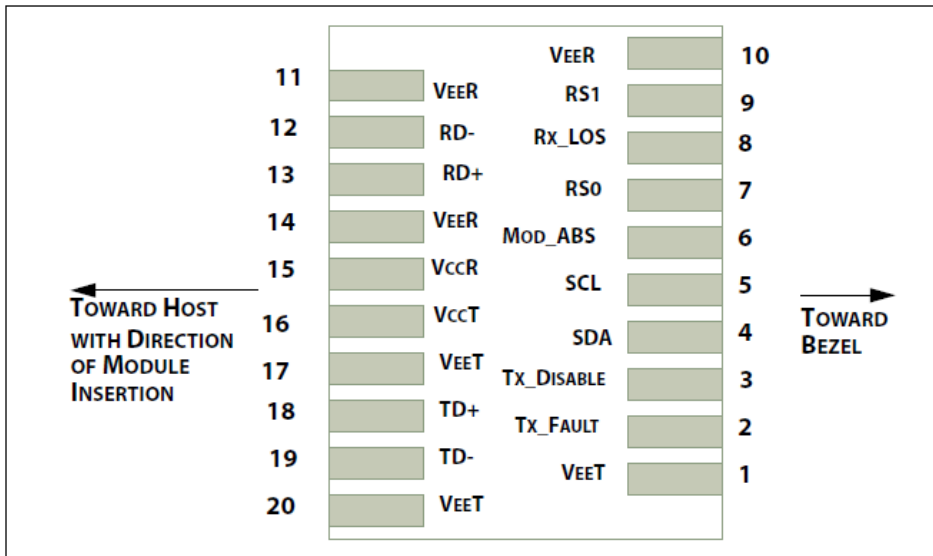
The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.



Pin Definitions



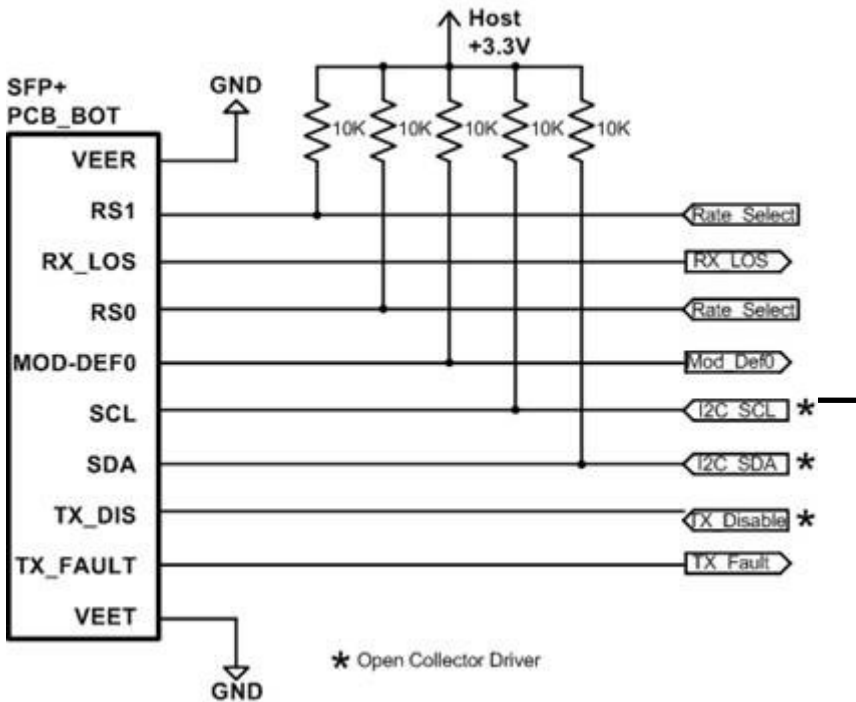
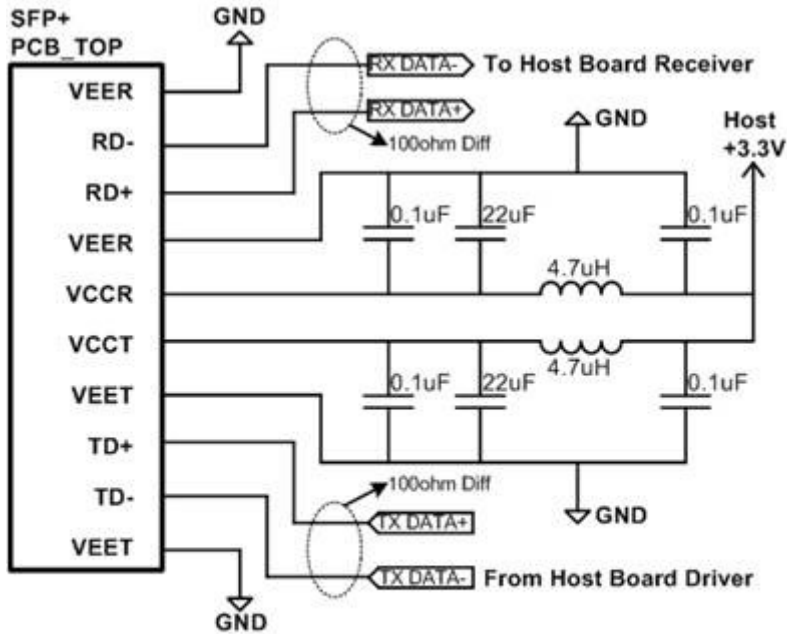
Pin Descriptions

PIN	Logic	Symbol	Name / Description	Note
1		VeeT	Module Transmitter Ground	1
2	LVTTL-O	TX_Fault	Module Transmitter Fault	2
3	LVTTL-I	TX_Dis	Transmitter Disable; Turns off transmitter laser output	
4	LVTTL-I/O	SDA	2-Wire Serial Interface Data Line	2
5	LVTTL-I	SCL	2-Wire Serial Interface Clock	2
6		MOD_ABS	Module Definition, Grounded in the module	
7	LVTTL-I	RS0	Receiver Rate Select	
8	LVTTL-O	RX_LOS	Receiver Loss of Signal Indication Active LOW	
9	LVTTL-I	RS1	Transmitter Rate Select (not used)	
10		VeeR	Module Receiver Ground	1
11		VeeR	Module Receiver Ground	1
12	CML-O	RD-	Receiver Inverted Data Output	
13	CML-O	RD+	Receiver Data Output	
14		VeeR	Module Receiver Ground	1
15		VccR	Module Receiver 3.3 V Supply	
16		VccT	Module Receiver 3.3 V Supply	
17		VeeT	Module Transmitter Ground	1
18	CML-I	TD+	Transmitter Non-Inverted Data Input	
19	CML-I	TD-	Transmitter Inverted Data Input	
20		VeeT	Module Transmitter Ground	1

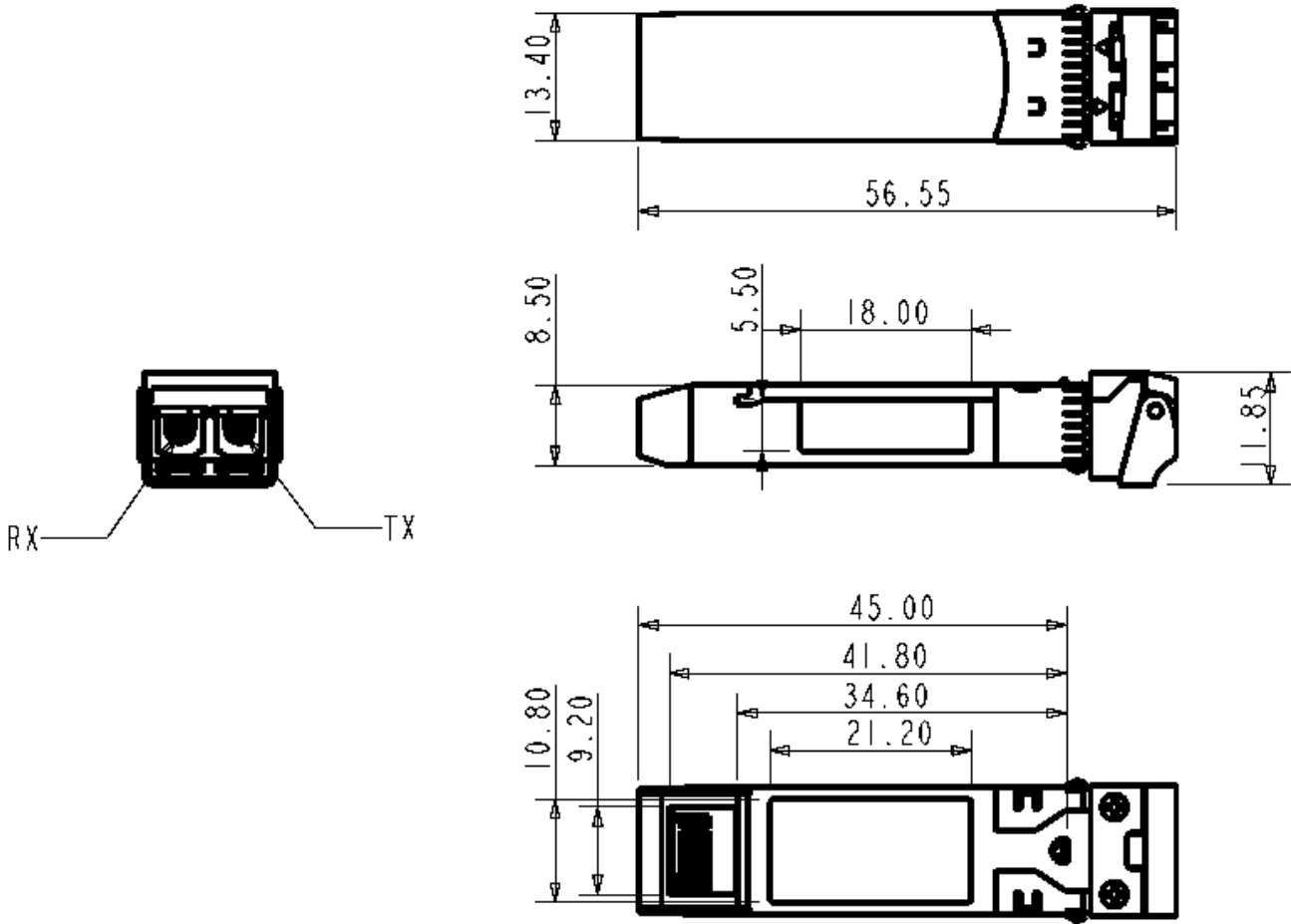
Notes:

1. Module ground pins GND are isolated from the module case.
2. Shall be pulled up with 4.7K-10Kohms to a voltage between 3.15V and 3.45V on the host board.

Recommended Interface Circuit



Mechanical Dimensions



Ordering information

Part Number	Product Description
i28-850-100m	25Gbps, 850nm; SFP28, OM4,MMF 100m, DDM 0°C ~ +70°C